

# Research Associate for the Design of Energy-Efficient RISC-V Subsystems for IoT Nodes (m/f/d)

Technische Fakultät, Erlangen, TV-L E 13, Vollzeit, Befristete Anstellung: 36 Monate,  
Bewerbungsschluss: 28.11.2024

## Your Workplace

The Institute of Information Technology at the Faculty of Engineering of Friedrich-Alexander-University Erlangen-Nürnberg (FAU) specializes in the design of digital circuits and is one of the leading institutions in this field. Here, innovative approaches and methods are developed to create efficient and high-performance digital systems. The institute places great emphasis on practical research and promotes collaboration with industry to ensure the transfer of knowledge and technologies. The close cooperation with the Fraunhofer Institute for Integrated Circuits IIS provides doctoral candidates with unique opportunities to extend their research activities beyond the purely academic context and gain practical experience in the industry. With modern laboratories and highly qualified professionals, the institute provides optimal conditions for students and researchers to develop cutting-edge solutions in the field of digital circuit technology.

## Benefits: We Have a Lot To Offer

- Regular promotion to the next level and increase in salary pursuant to the collective bargaining agreement for the public service of the German Länder (TV-L) or remuneration pursuant to the Bavarian Public Servants Remuneration Act (BayBesG) plus an additional annual bonus
- 30 days annual leave at five working days per week with additional free days on December 24 and 31
- Occupational pension scheme and asset accumulation savings scheme

## Your Tasks

- Hardware and hardware-related software for methods and strategies for battery-powered IoT nodes with an optimized RISC-V processor subsystem for ultra-low power consumption (wake-up concept) in industrial applications
- Expansion of collaboration with the Fraunhofer IIS for integrated digital systems
- Publication and presentation of research results
- Supervision of Bachelor's and Master's theses, as well as conducting teaching exercises, seminars, or computer labs

## Your Profile

- Completed academic degree (Master's/Diploma) in digital circuit technology or a related field
- Good knowledge of SystemVerilog and/or VHDL as well as FPGAs

- Excellent English skills (minimum C2 level)

### **Additional Information**

There is an opportunity for a doctoral degree in the field of Digital System-on-Chip Design.

### **Interessiert?**

Die vollständige Stellenausschreibung sowie alle Infos zum Bewerbungsverfahren finden Sie hier:

